



The Manufacture of the CMS Tracker Front-End Driver

J.A. Coughlan ¹, R. Bainbridge ², D. Ballard ¹, I. Church ¹, E. Corrin ², C.P. Day ¹, C. Foudas ², E.J. Freeman ¹, J. Fulcher ², W.J.F. Gannon ¹, G. Hall ², R.N.J. Halsall ¹, G. Iles ², J. Leaver ², M. Noy ², M. Pearson ¹, M. Raymond ², I. Reid ³, G. Rogers ¹, J. Salisbury ¹, S. Taghavi ¹, I.R. Tomalin ¹, O. Zorba ²

¹ CCLRC Rutherford Appleton Laboratory, Oxfordshire, UK
² Imperial College, London, UK
³ Brunel University, London, UK

J.Coughlan@rl.ac.uk

Abstract

The Front-End Driver (FED) is a 9U 400mm VME64x card designed for reading out the CMS silicon tracker. The FED was designed to maximise the number of channels that could be processed on a single 9U board and has a mixture of optical, analogue (96 ADC channels) and digital, Field Programmable Gate Array (FPGA), components. Nevertheless, a total of 440 FED boards are required to readout the entire tracker. Nearly 20 full-scale prototype 9U FED boards have been produced to date. This paper concentrates on the issues of the large-scale manufacture and assembly of PCBs. It also discusses the issues of production testing of such large and complex electronic cards.