

10th Workshop on Electronics for LHC and future Experiments, Boston USA

Tuesday, 14 September 2004

Parallel session A1 (11:00 - 14:00)

time	[id] title	presenter
11:00	[72] A1.1 Qualification of the Front End Cards for the CMS ECAL	MARTIN GASTAL
11:25	[73] The CMS ECAL Very Front End Electronics Production and Test	ALESSANDRO NARDULLI
11:50	[74] Radiation effects and mitigation strategies for modern FPGAs	MATT STETTLER
12:15	[75] Irradiation tests of ROHM 0.35um ASIC and Actel Anti-fuse FPGA for the ATLAS muon endcap level-1 trigger system	CHIKARA FUKUNAGA
12:40	Lunch and poster session	