

10th Workshop on Electronics for LHC and future Experiments, Boston USA

Tuesday, 14 September 2004

Plenary session P2 (09:00 - 11:00)

time	[id] title	presenter
09:00	[70] Field Programmable Gate Arrays in 2004	PETER ALFKE
09:45	[71] Enabling Technologies for High Performance Chip Scale Packaging	TOM BUCK
10:30	Coffee and poster session	