

10th Workshop on Electronics for LHC and future Experiments, Boston USA

Tuesday, 14 September 2004

Parallel session B2 (14:00 - 16:10)

time	[id] title	presenter
14:00	[84] Design and performance of a PCI interface with four 2 Gbit/s serial optical links	STEFAN HAAS
14:25	[85] The L0 Off Detector Electronics (ODE) for the LHCb muon spectrometer	GIULIETTO FELICI
14:50	[86] Tests of the boards generating the ECAL Trigger Primitive in CMS: from the On-Detector electronics to the Off-Detector electronics system	PASCAL PAGANINI
15:15	[87] Design of a z-Vertex Trigger and its Operation Experience in the H1 Experiment at HERA	MAX CHRISTOPH URBAN
15:40	Coffee and poster session	