

10th Workshop on Electronics for LHC and future Experiments, Boston USA

Wednesday, 15 September 2004

Parallel session B6 (16:10 - 17:50)

time	[id] title	presenter
16:10	[32] Novel bridges for PC-based VME control	NICOLA PAOLI
16:35	[33] The LHC PC Rack Project	FRED WICKENS
17:00	[34] The Final prototype of the Fast Merging Module (FMM) for readout status processing in CMS DAQ	ATTILA RACZ
17:25	[35] TTC Interface Module for ATLAS Read Out Electronics : Final production version based on Xilinx FPGA devices	, Matt Warren