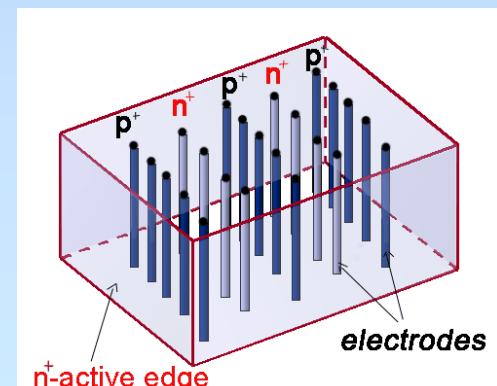


3D Silicon & Radiation Hardness

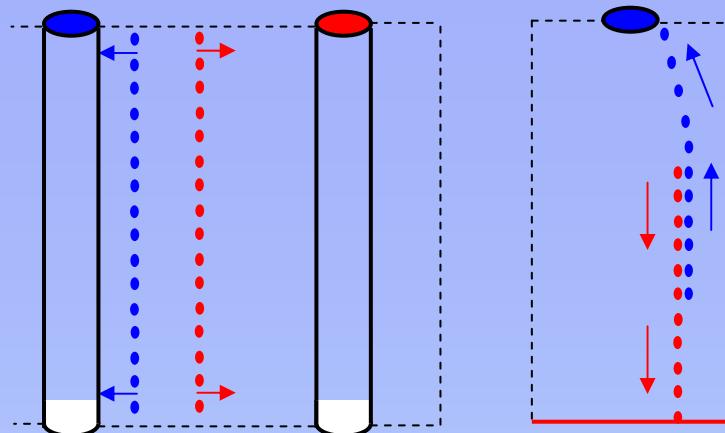
Outline:

- Why is 3D Silicon Rad Hard (at high fluences)??
- Discussion
- Results
- Conclusions and future plans

C Da Via', J Hasi, A Kok,
S Watts (Brunel University)
G Anelli, M Deile, P Jarron, J
Kaplon, J Lorzano (CERN)
S Parker (Hawaii)
C Kenney E. Westbrook (MBC)
J Morse (ESRF)



OBVIOUS ANSWER
is 'yes because 3D
has':



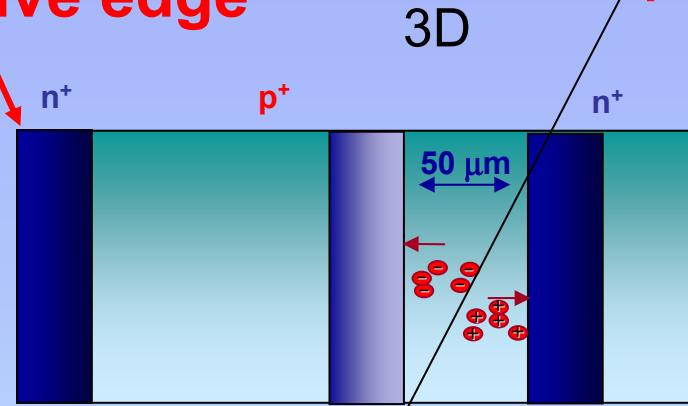
- ❖ Short collection distance ($50 \mu\text{m}$)
- ❖ High average e-field with moderate V_{bias}
- ❖ Parallel charge collection
- ❖ Always use full substrate thickness (MIP $\sim 80 \text{ e}^-/\mu\text{m}$)

- ❖ Drawback: higher Capacitance
(measured $200 \text{ fF}/121\mu\text{m}/\text{electrode}$)

**Could thin [O] rich
planar Silicon devices be competitive?**

3D VERSUS PLANAR

Active edge



3D

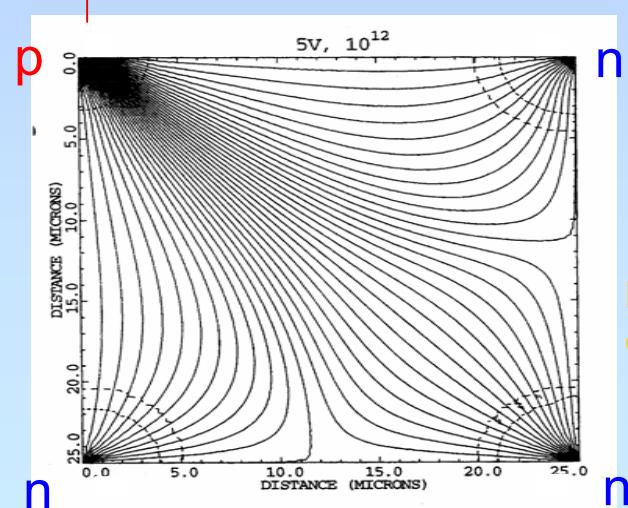
particle

PLANAR

microcracks,
chips induce surface
leakage current

$\sim 500 \mu\text{m}$

Collecting electrode



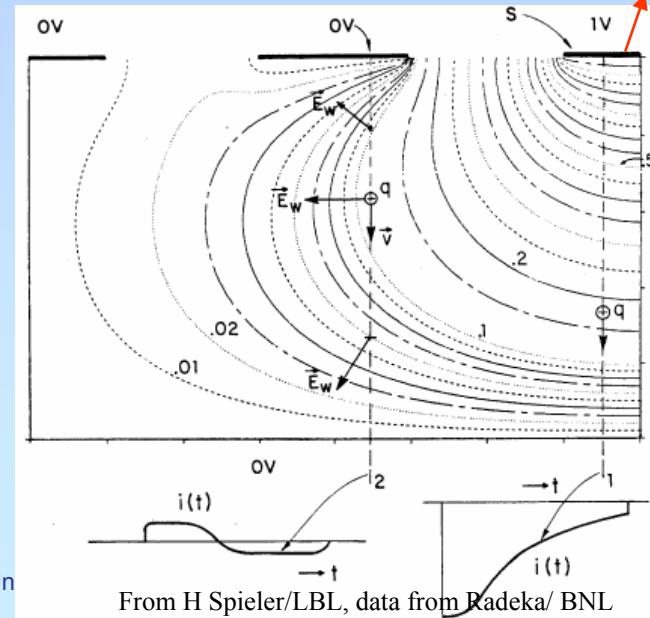
MEDICI simulation
of a 3D structure

02/08/2004

Drift lines parallel to the surface

IWORLD 2004 C. Da Via Brunel Un

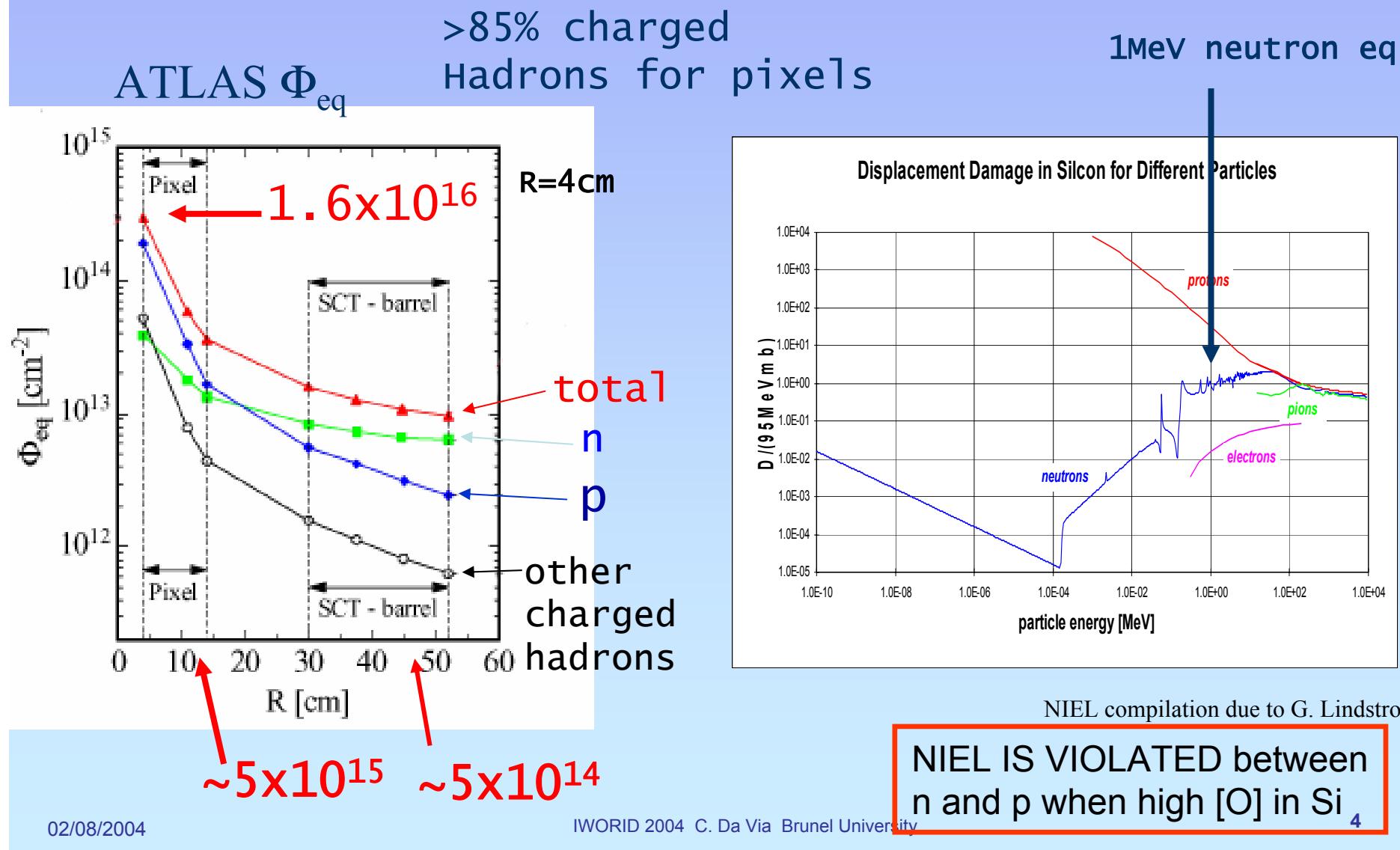
Collecting electrode



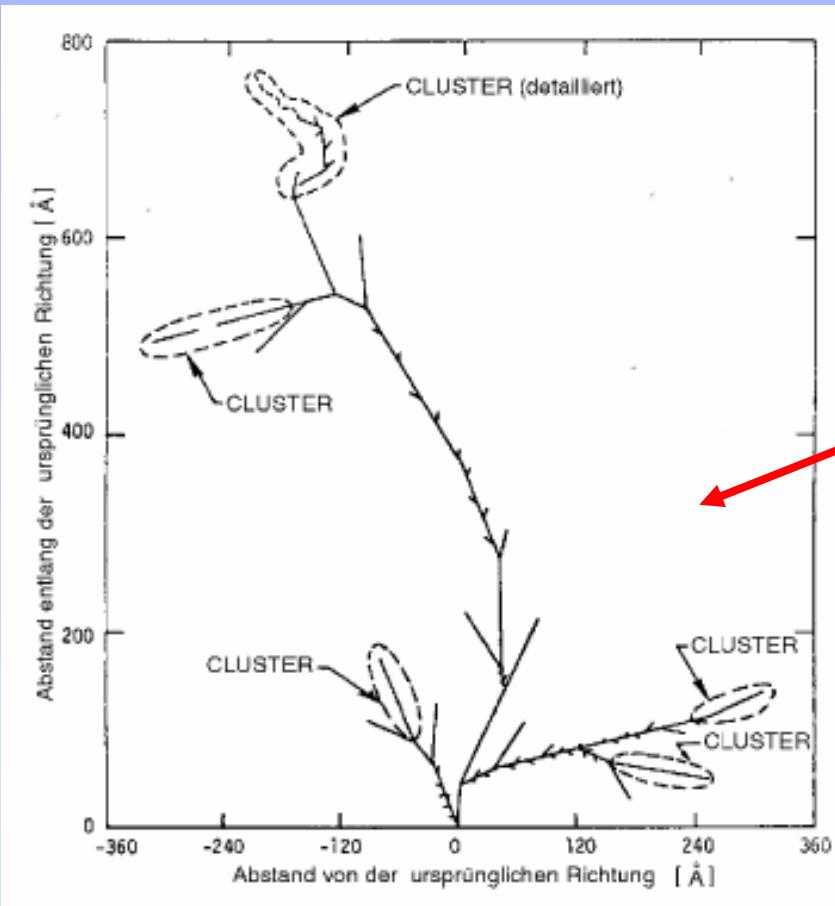
From H Spieler/LBL, data from Radeka/ BNL

3

RADIATION DAMAGE IN HIGH ENERGY PHYSICS EXPERIMENTS: Multiple particle environment



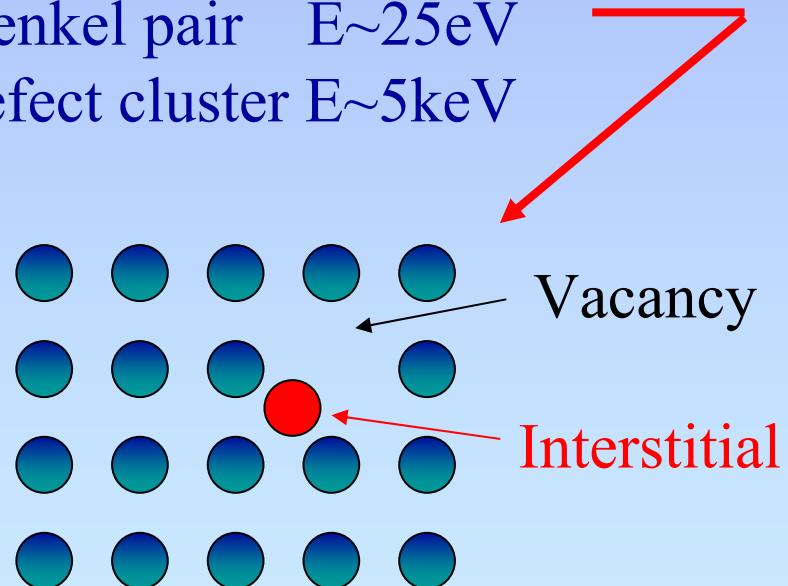
RADIATION INDUCED BULK DAMAGE in Si



Van Lint 1980

Primary Knock on Atom

Displacement threshold in Si:
Frenkel pair $E \sim 25\text{eV}$
Defect cluster $E \sim 5\text{keV}$



AT 1×10^{15} n/cm² :

Studies mainly from
RD48/ROSE Collaboration

STANDARD 300μm n-type SILICON at 10^{15} n/cm²
10 years of operation at $L=10^{34}$ cm⁻²s⁻¹ at R=4 cm

EFFECTIVE DRIFT LENGTH
Due to charge trapping

$\sim 150\mu\text{m}$ e⁻ $\sim 50\mu\text{m}$ h

- ❖ Signal formation
- ❖ Charge sharing
- ❖ Speed

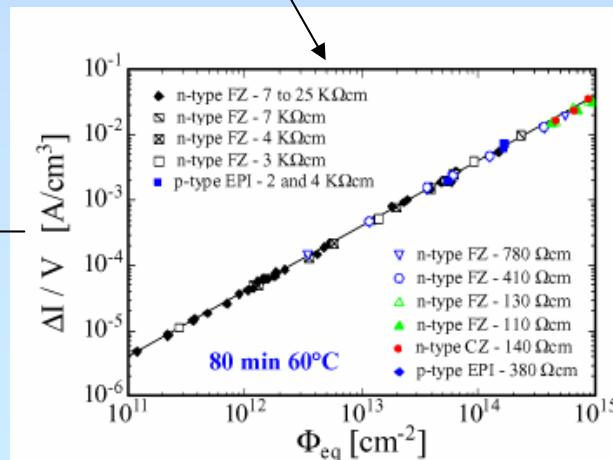
**SPACE CHARGE
TYPE INVERSION**

-ve N_{eff} ($10^{13}/\text{cm}^3$) $\sim V_{FD}$ (5000V) $\sim \Phi$
depletion from n-contact (e-field)

- ❖ Double junction
- ❖ Charge diffusion

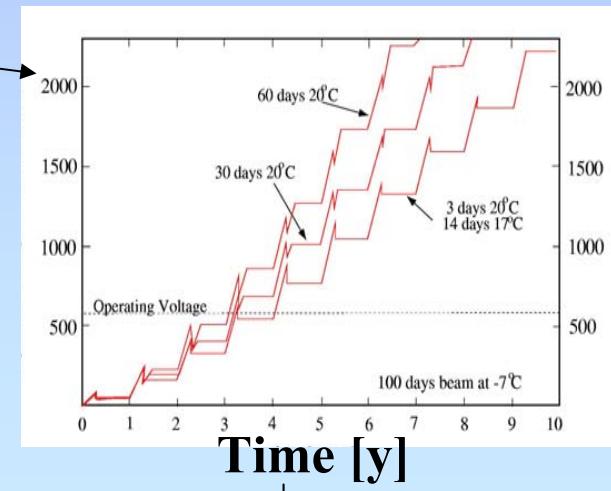
REVERSE ANNEALING
LEACKAGE CURRENT

INCREASE OF -ve N_{eff} temp. dep
prop to Φ ($I/V \sim 5 \times 10^{-17} \Phi$)



02/08/2004

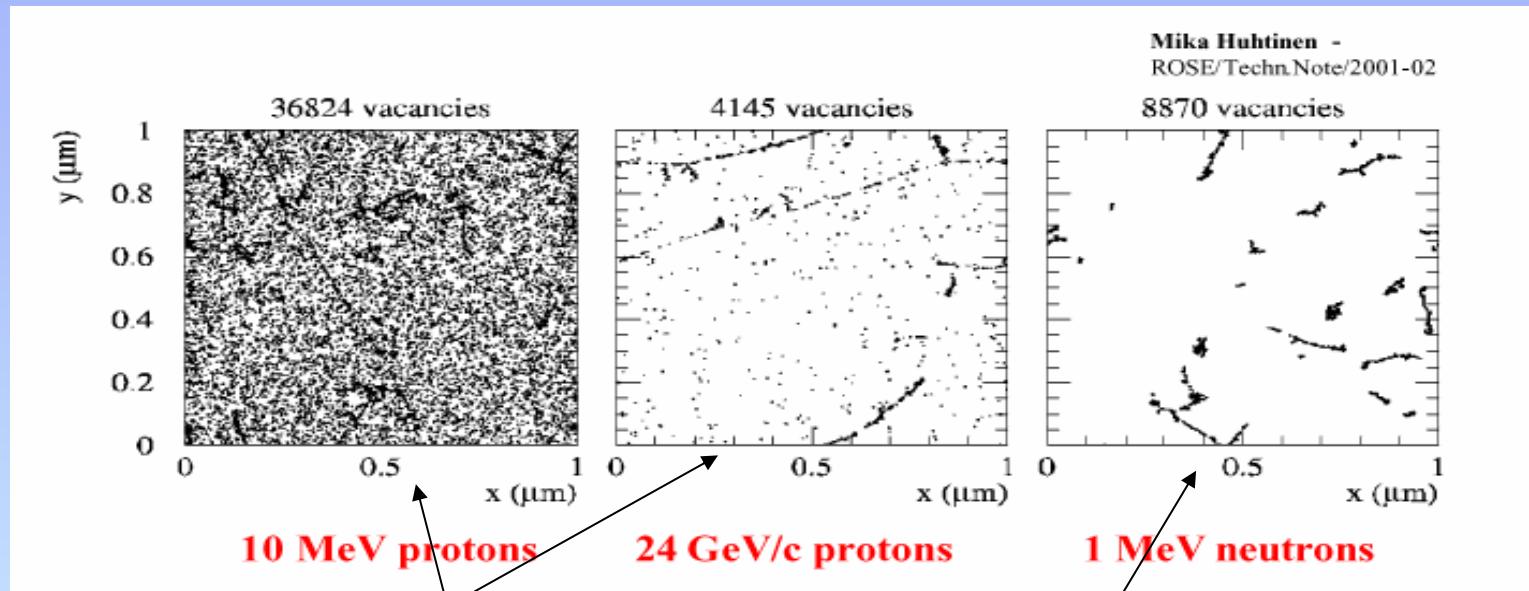
- ❖ Noise
- ❖ Thermal runaway



- ❖ Maintenance

NEUTRON PROTON PUZZLE

PROTONS → POINT DEFECTS

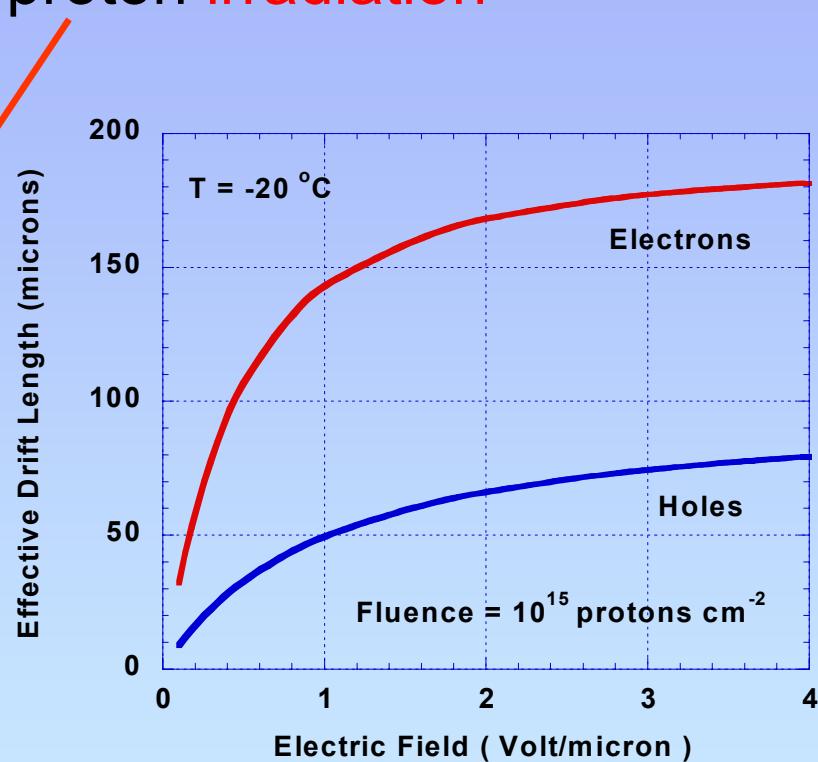
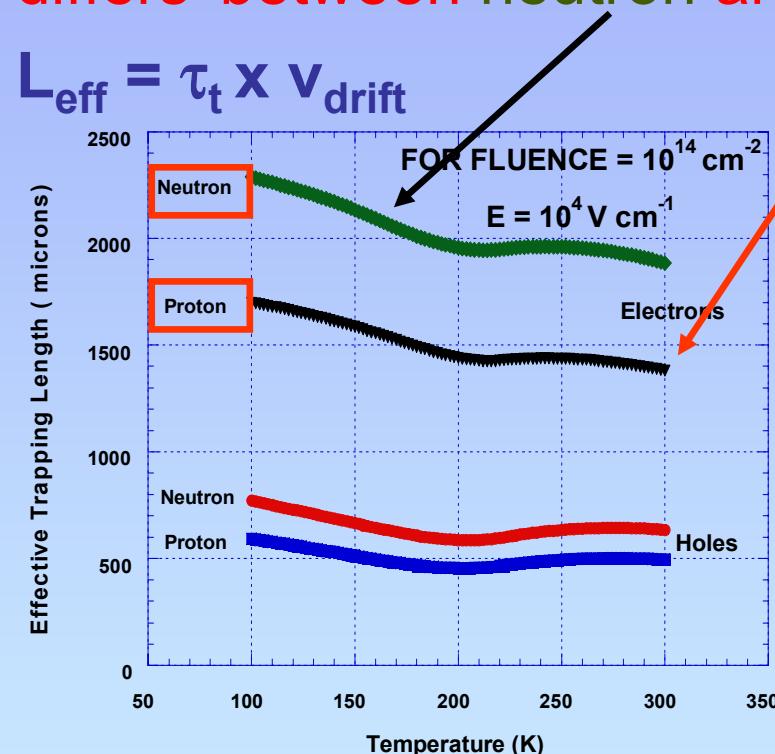


Signal can degrade because of:

reduced depleted volume
trapping

EXAMPLE OF NIEL VIOLATION

Effective trapping time τ_t for e and h measured by Kramberger differs between neutron and proton irradiation



- ❖ Collect electrons
- ❖ Beware protons!
- ❖ Work at v_{drift} saturated

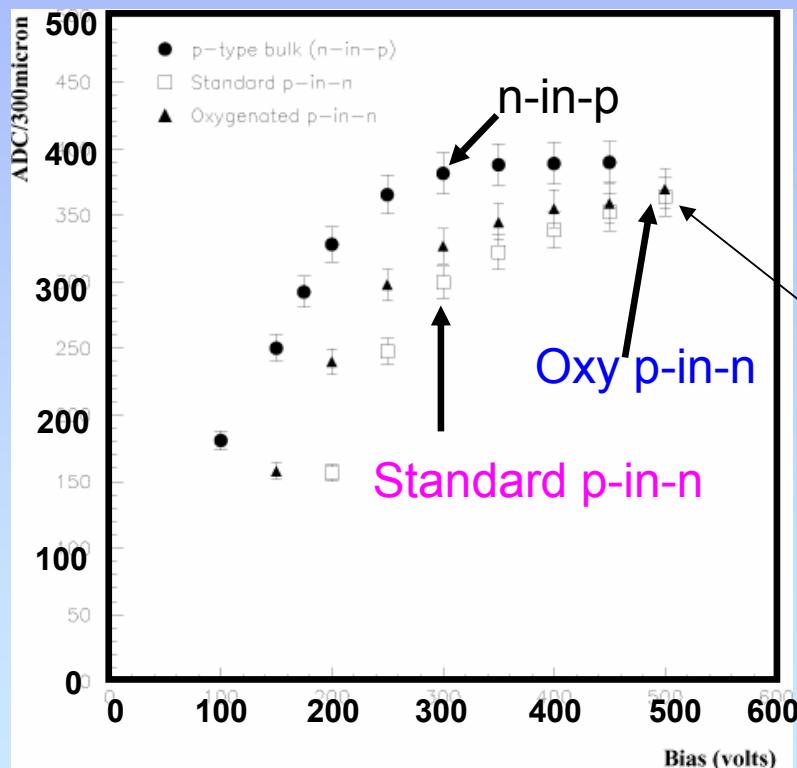
Trapping times from Kramberger et al. NIMA 481 (2002) 100
Simulations CDV and S.Watts NIM A 501(2003) 138 (Vertex 2001)

CONSIDERATION ON CCE and SIGNAL

25ns electronics

$3 \times 10^{14} \text{ n/cm}^2$

T=-17°C



Casse' et al. NIMA 487 (2002) 465-470

2 REGIMES:

1- LOW FLUENCE- DEPLETION VOLUME DOMINATES

Neutrons= bad

Protons =good

Oxygen helps

2- HIGH FLUENCE –TRAPPING DOMINATES

Neutrons = bad

Protons = worse!

Oxygen does not help

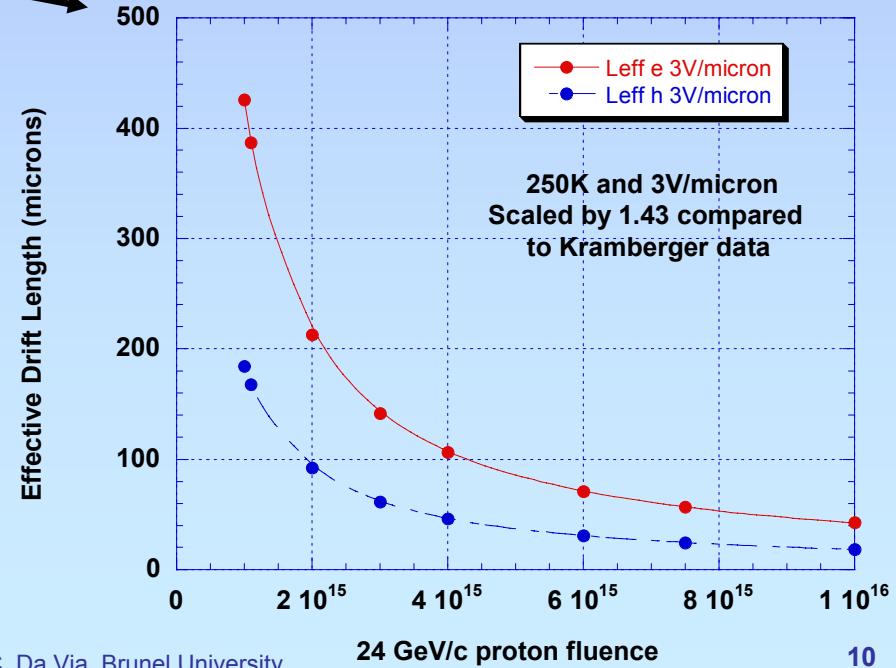
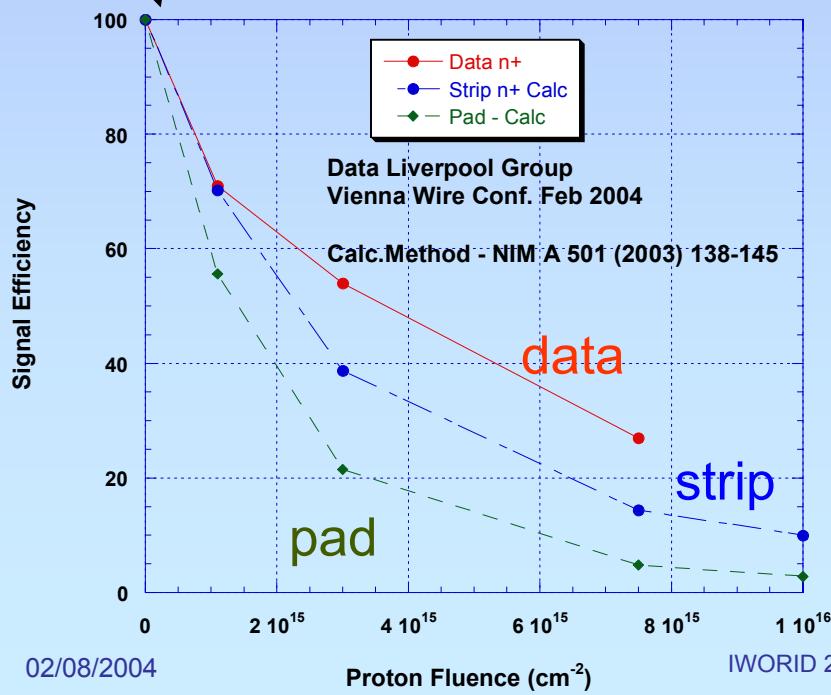
THE SIGNAL IS DIRECTLY PROPORTIONAL TO L_{eff}

$$S = \int_0^d q e^{-\frac{x}{L_{\text{eff}}}} W(x) dx = q L_{\text{eff}} \left(1 - e^{-\frac{d}{L_{\text{eff}}}} \right) K$$

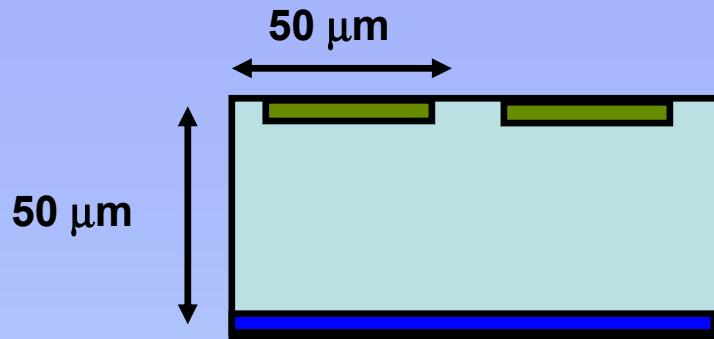
Positive!
The signal flattens at
high ϕ

$$S \propto L_{\text{eff}} = v_{\text{drift}} \times \tau_{\text{trap}} \propto \frac{1}{N_{\text{traps}}} \propto \frac{1}{\Phi}$$

Should then planar
detectors be thinner?



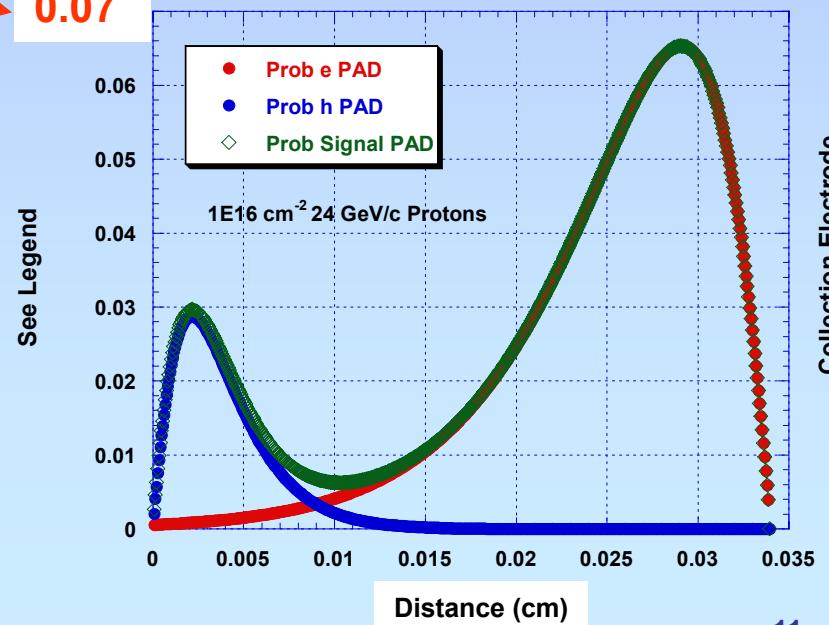
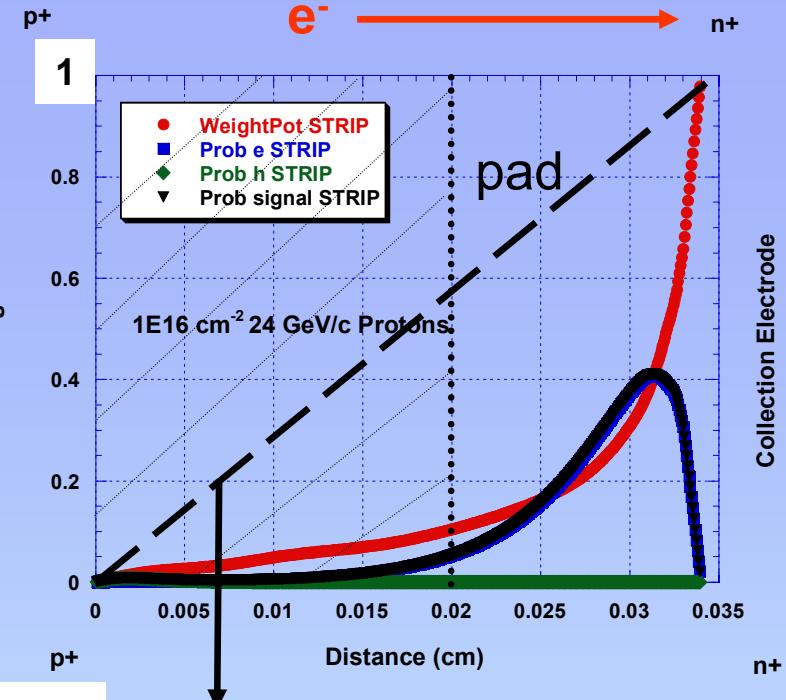
ONLY IF PITCH PROP. SMALLER



If same pitch → Thin detector = PAD!

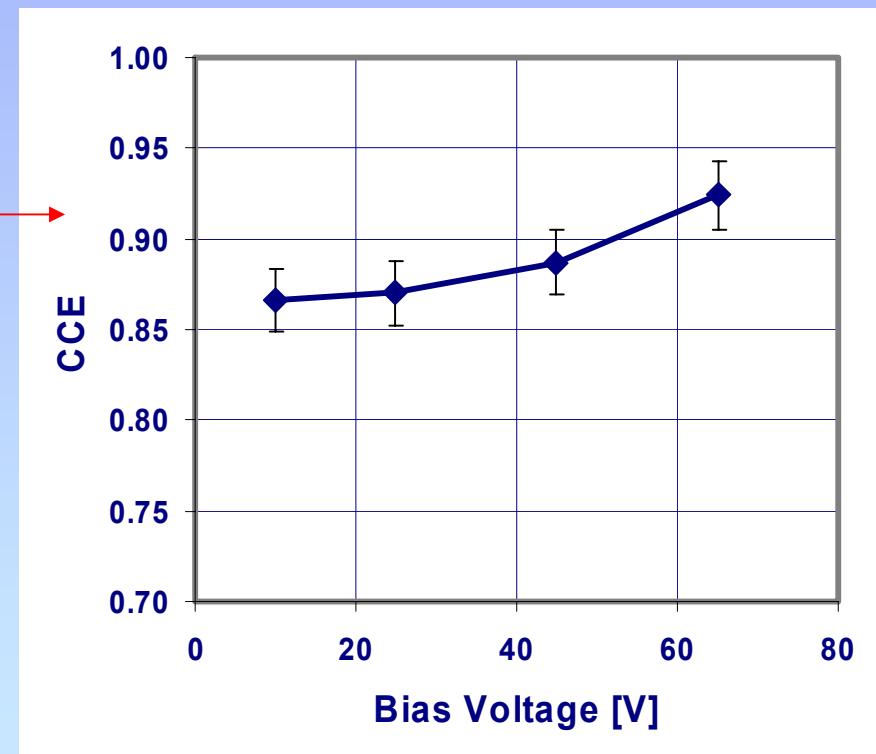
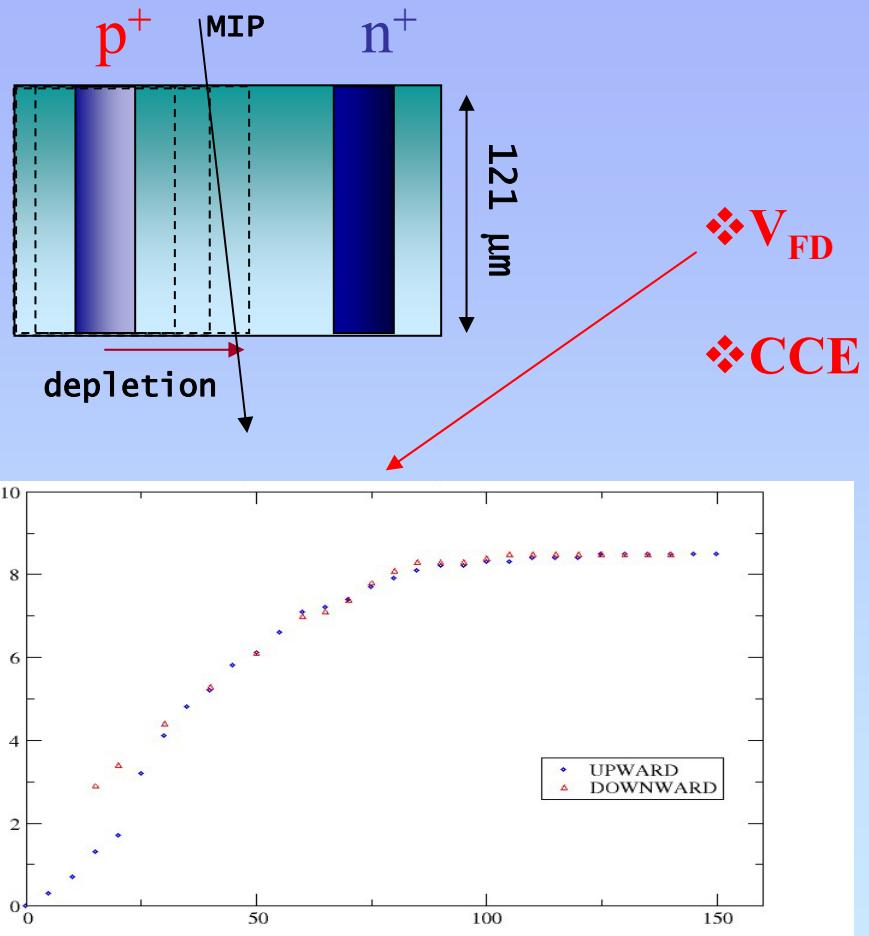
Better to work under-depleted
With a thicker detector
to make use of the steepness
of the weighting potential (which
only depends on the electrode
width/thickness ratio)

Scale!! → 0.07



3D - RADIATION HARD TESTS

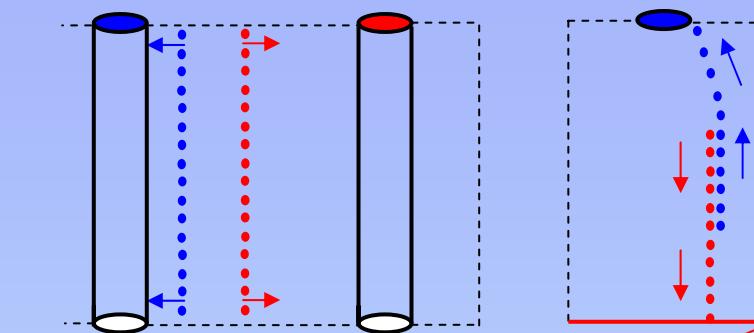
Room Temperature NON oxygenated



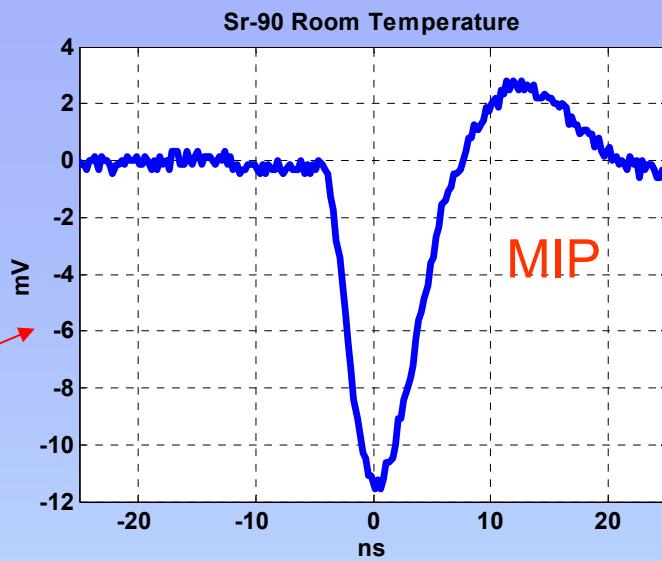
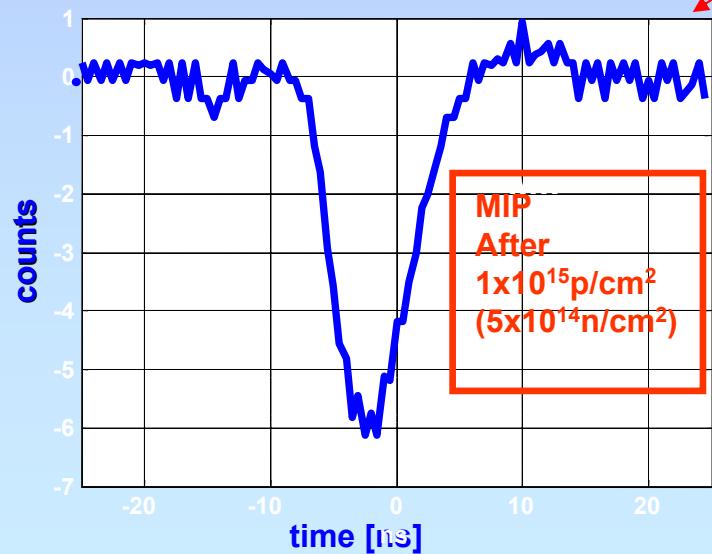
❖ IEEE Trans on Nucl Sci 48 (2001) 1629
 ❖ Nucl.Instrum.Meth.A509:86-91,2003

joined work Brunel, Cern, Hawaii to be published

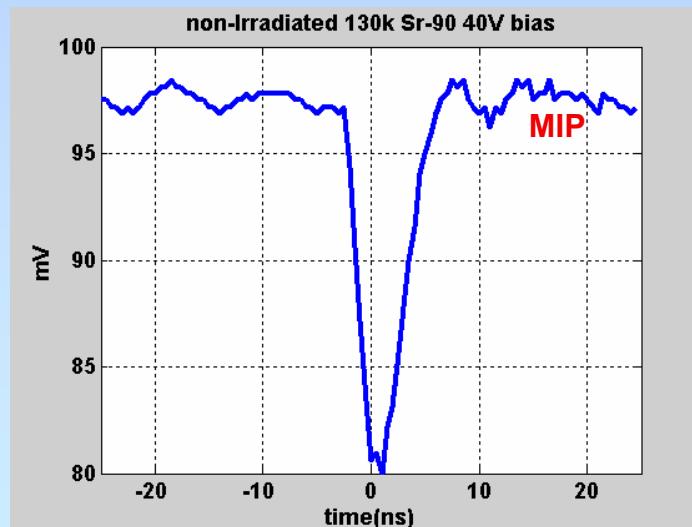
SPEED (SLHC bx =12.5ns)



3.5 ns at 300K before and after ϕ



1.5 ns at 130K



*Fast Electronics CERN MIC : P. Jarron et al. NIM A 377 (1996) 435

In Conclusion:

- 3D wins compared with equal electrode spaced Si if pitch is not smaller despite larger 3D capacitance (thin substrate → higher C in planar devices as well)
- S/N better for 3D because one can use all the charge deposited in the substrate

A lot to do!

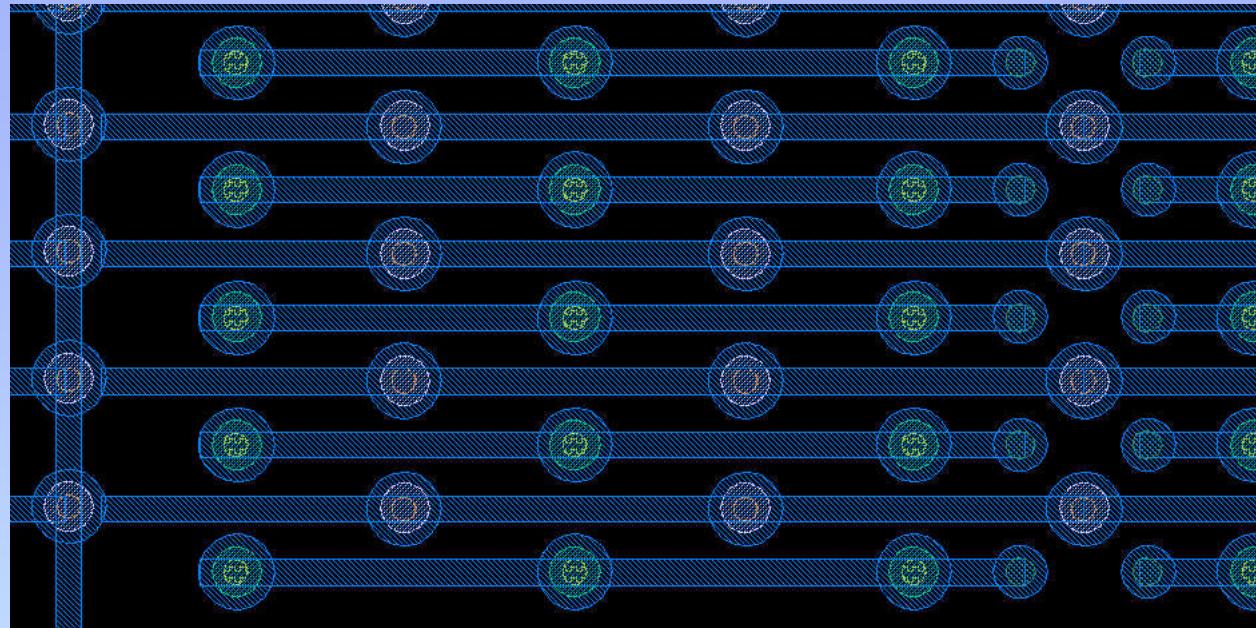
- At high fluences charged hadrons are lethal for trapping
- Oxygen does not help when trapping is dominating
- Signal depends on L_{eff} and on $1/\Phi$

Very inspiring article by Kramberger et al. NIM A 511 (2003)82

Forthcoming 3D tests:

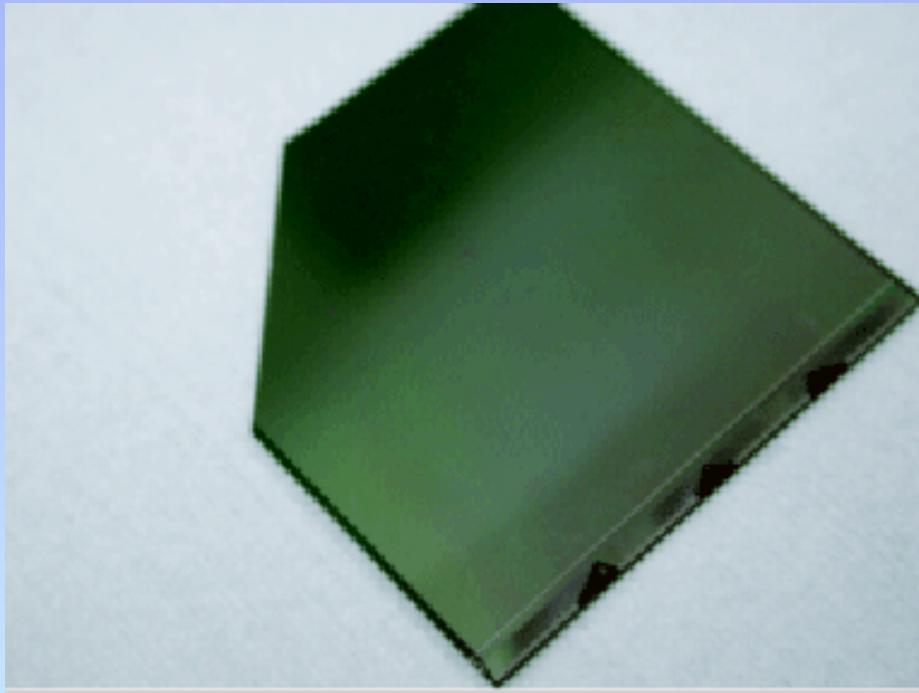
- ❖ ATLAS pixel compatible (radiation limit)
- ❖ TOTEM 3 x 4 cm² (pure 3D, planar with 3D edges)
- ❖ Speed measurement with 0.13 µm CMOS
- ❖ Radiation tests for active edge (planar/3D)

Atlas Upgrade Pixel Cell Design



- 3 Collection and 3 Field electrodes per Cell
- Depletion Distance Under 75 μm
- Electrode Area 6 % of Cell
(improved etching may reduce this)

3D – TOTEM



- Full size active edge sensor $3 \times 4 \text{ cm}^2$
- Forthcoming tests at X5 and SPS