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**Detectors and electronics for Super-LHC:  
IRRADIATION RESULTS**

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# OUTLINE

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The presented results are from experiments performed by:

-**SMART** Collaboration (Bari, Firenze, Pisa, Padova, Perugia, Trieste, Trento)

-Department of Information Engineering (**DEI**), University of Padova

**Measurements are in progress ...**

## **1) Nuclear reactor irradiation in Lubiana:**

-**MCZ** n-type detectors (SMART)

-**Epitaxial** n-type detectors (SMART)

## **2) 24 GeV proton irradiation at CERN:**

-**Epitaxial** n-type detectors (SMART)

-**FZ thinned** n-type detectors (SMART)

- **0.13  $\mu\text{m}$  CMOS** technology (DEI)

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# DETECTORS

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## -MCZ detectors

**Substrate:** MCZ, n-type, 300  $\mu\text{m}$  thick,  $\rho = 0.6 \text{ k}\Omega\cdot\text{cm}$ , from Okmetic

**Detector processing:** IRST

## -Epitaxial detectors

**Epitaxial layer:** n-type, 50  $\mu\text{m}$  thick,  $\rho = 50 \Omega\cdot\text{cm}$ ,  
grown by ITME on CZ substrate

**Detector processing:** IRST

## -FZ thinned detectors

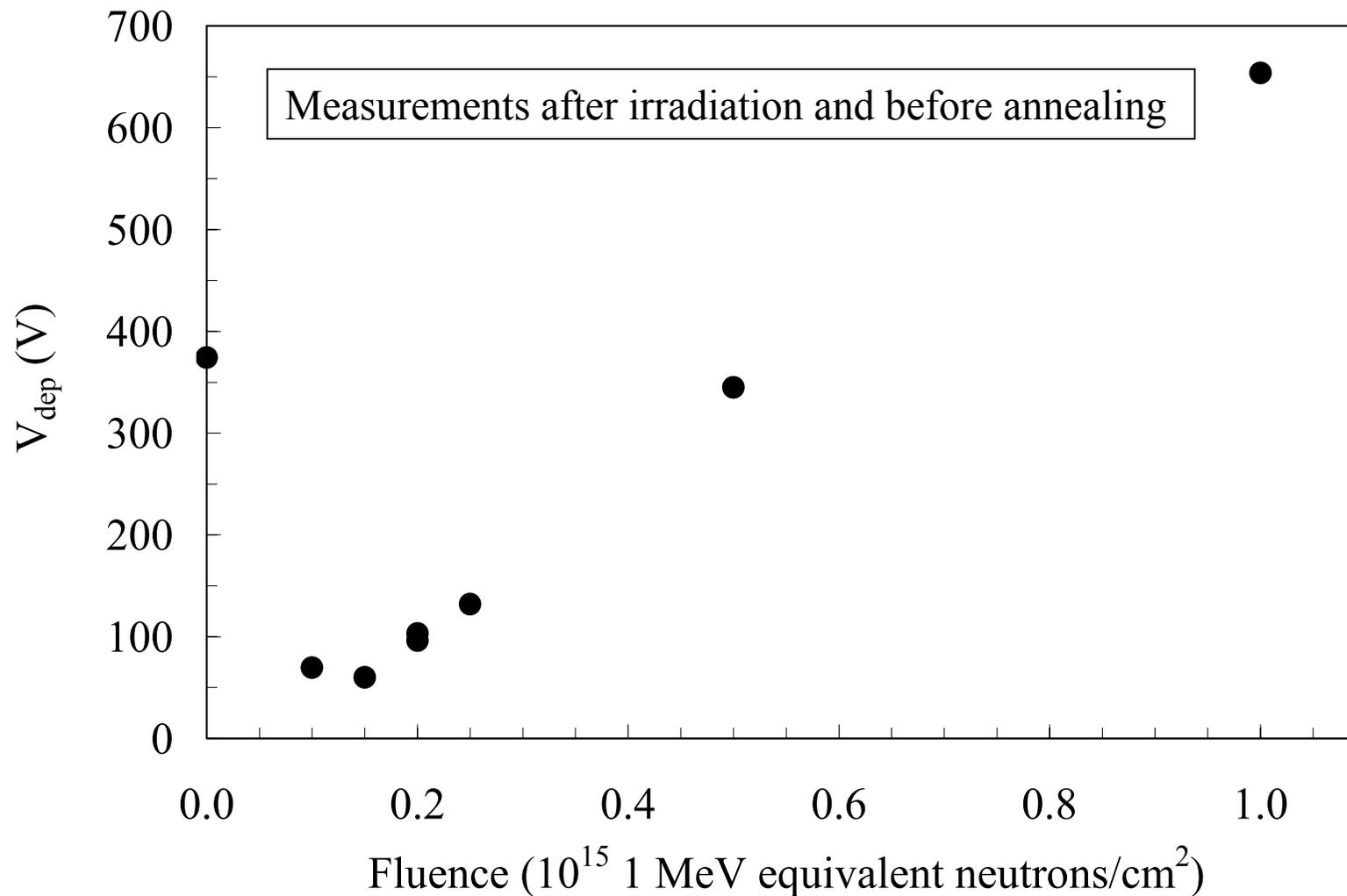
**Substrate:** FZ, n-type, 300  $\mu\text{m}$  thick,  $\rho = 6 \text{ k}\Omega\cdot\text{cm}$

**Thinning:** down to 50-100  $\mu\text{m}$  by Tetra Methyl Ammonium Hydroxide  
(TMAH) etching from backside: IRST

**Detector processing:** IRST

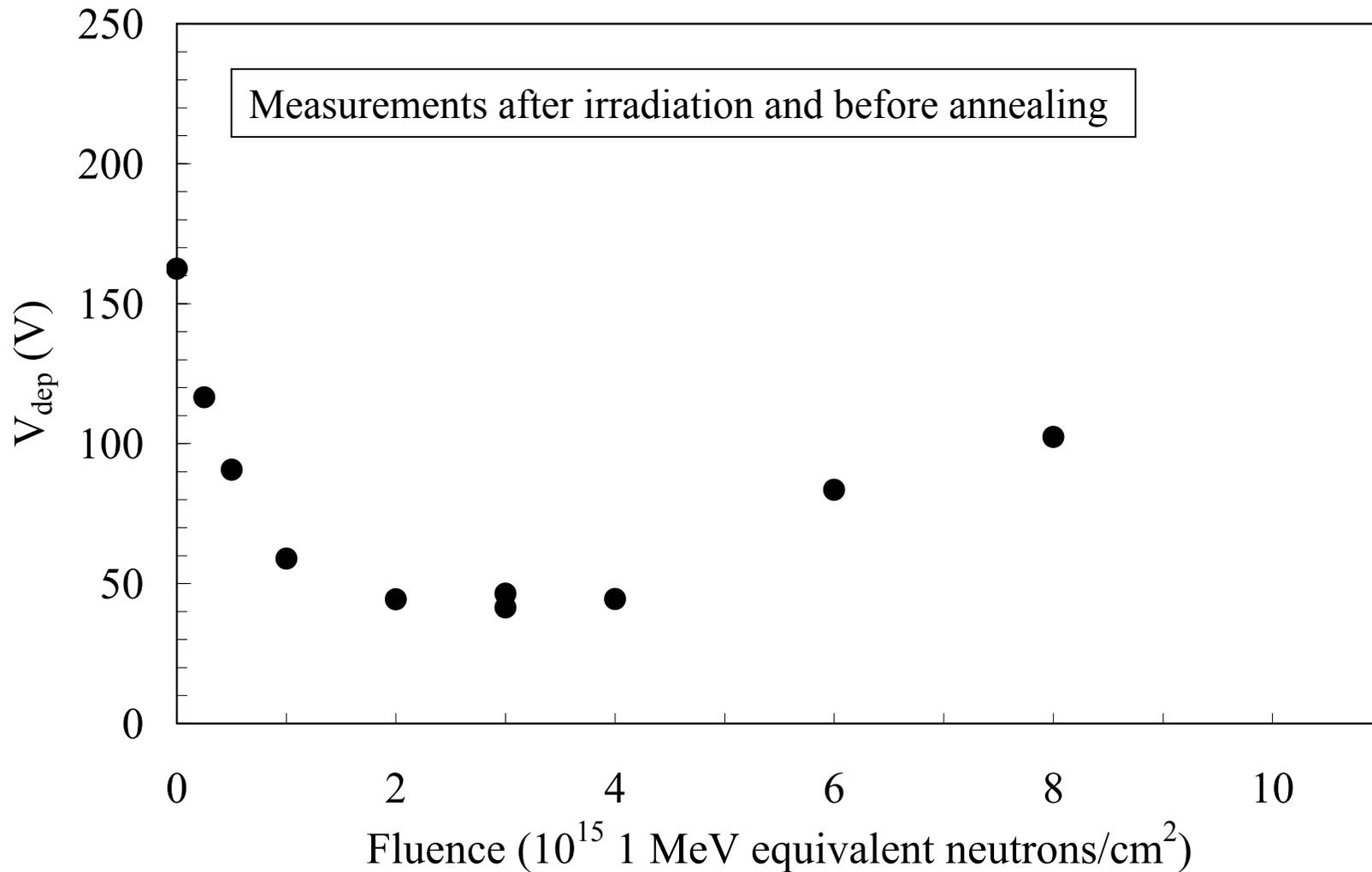
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## MCZ n-type 300 $\mu\text{m}$ thick detectors: neutron irradiation



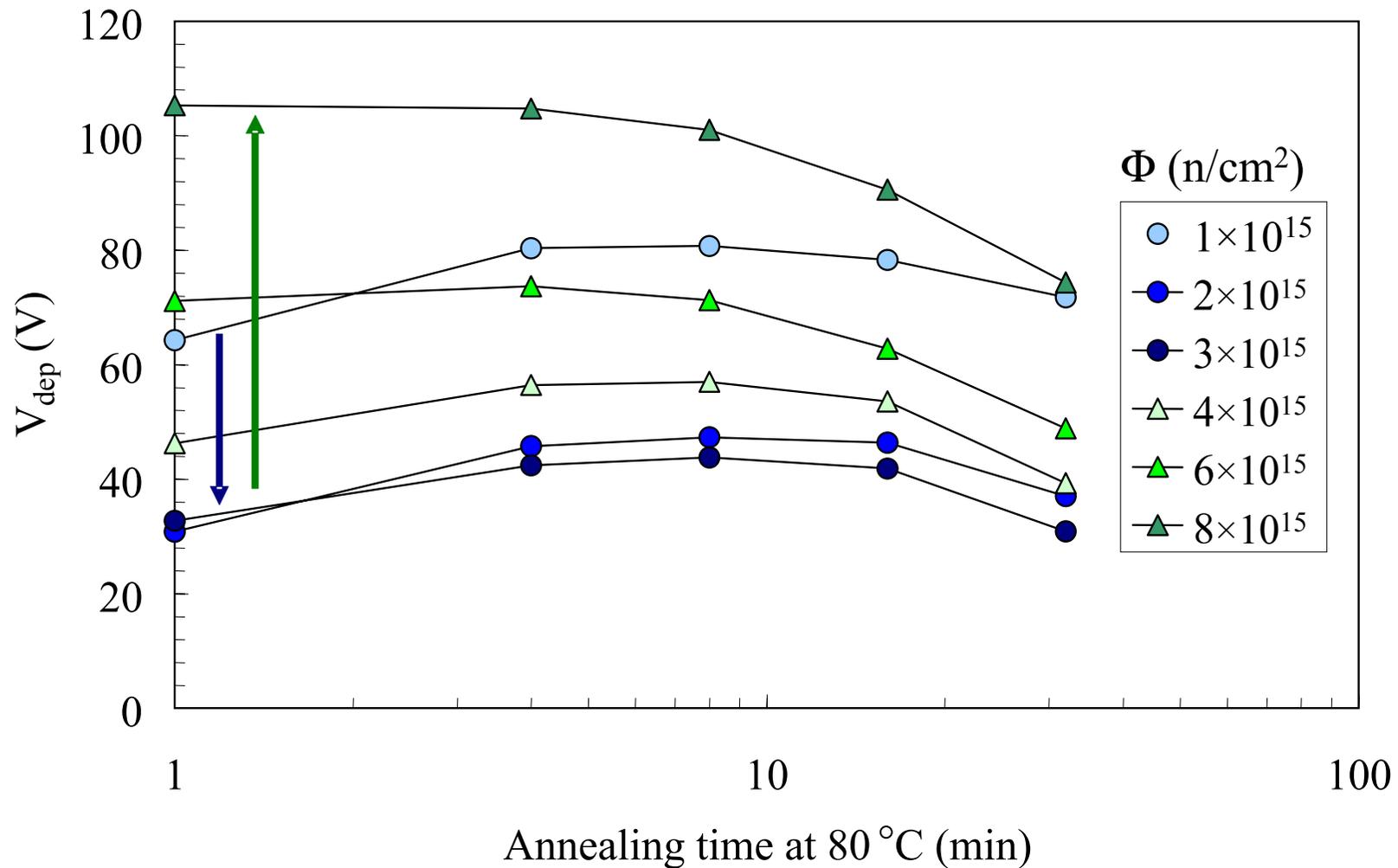
- The minimum of  $V_{\text{dep}}$  is reached at  $1-1.5 \times 10^{14}$  n/cm<sup>2</sup>.
- $V_{\text{dep}}$  is  $\approx 650$  at  $10^{15}$  n/cm<sup>2</sup>.

# Epitaxial n-type 50 $\mu\text{m}$ thick detectors: neutron irradiation



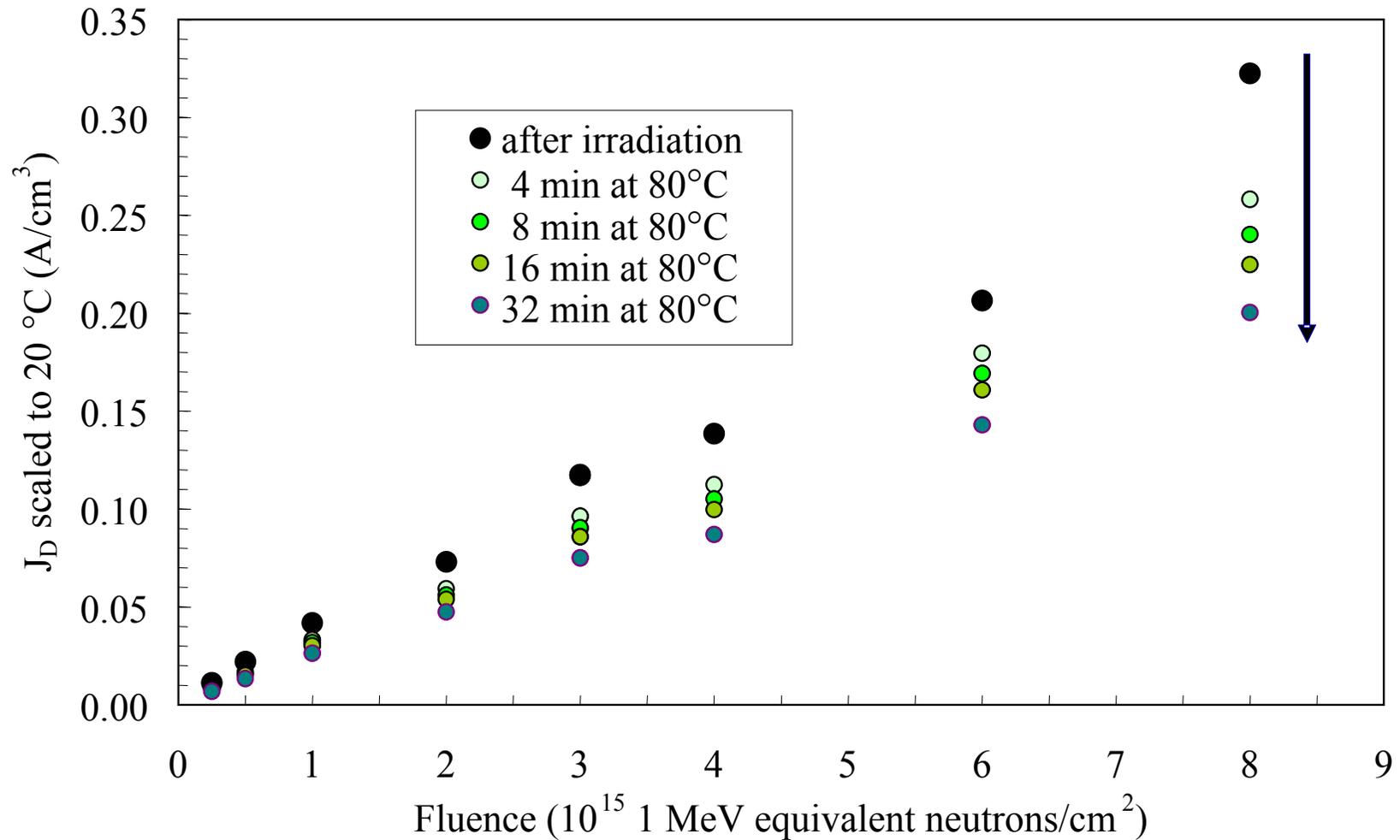
- The minimum of  $V_{\text{dep}} \approx 40\text{-}50$  V is reached at  $2\text{-}4 \times 10^{15}$  n/cm<sup>2</sup>.
- $V_{\text{dep}} < V_{\text{dep},0}$  at  $8 \times 10^{15}$  n/cm<sup>2</sup>.

# Epitaxial n-type 50 $\mu\text{m}$ thick detectors: neutron irradiation



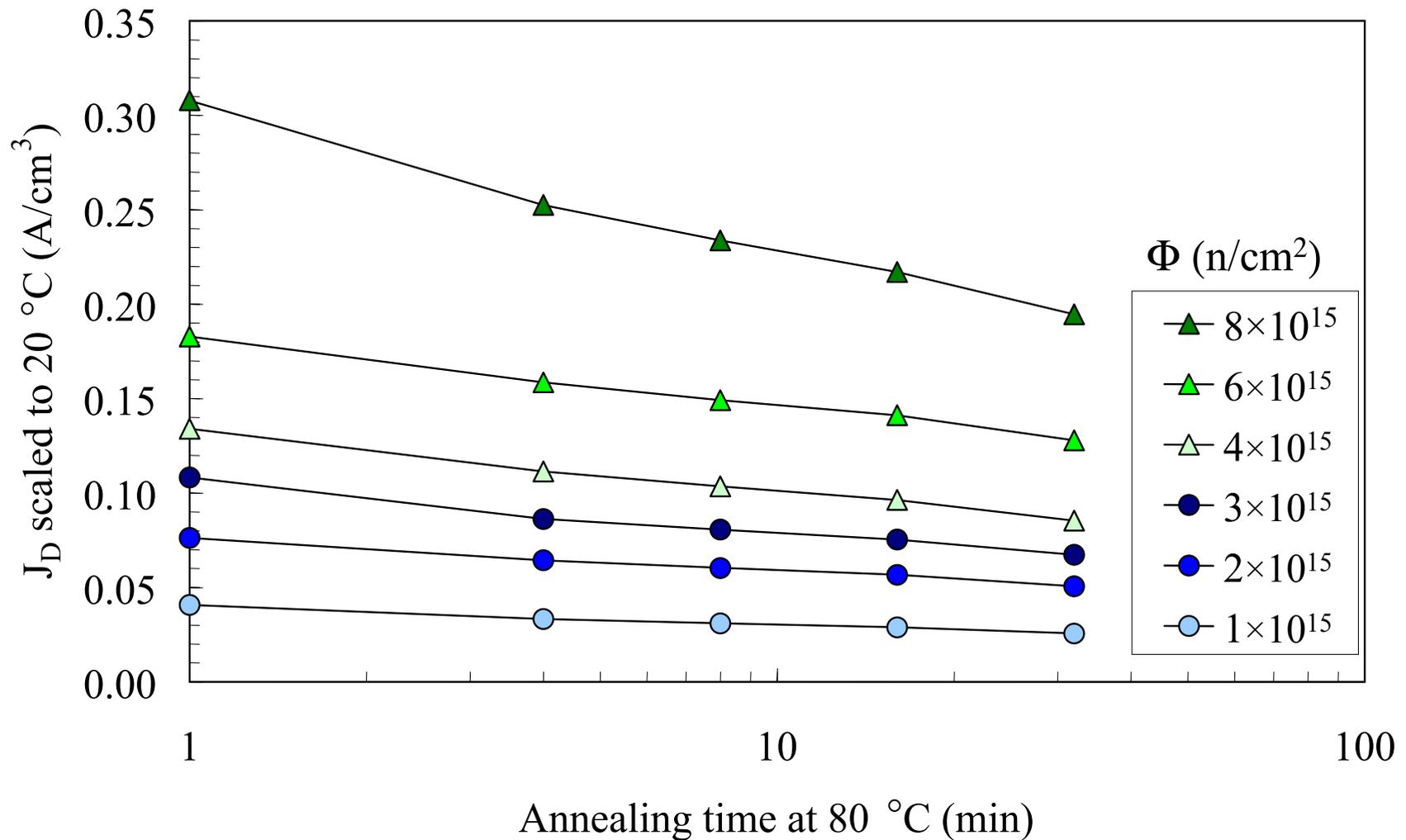
- $V_{\text{dep}}$  decreases for annealing times at 80 °C higher than 8 minutes.
- If this effect is due to deep acceptor generation, devices are not type inverted.

# Epitaxial n-type 50 $\mu\text{m}$ thick detectors: neutron irradiation



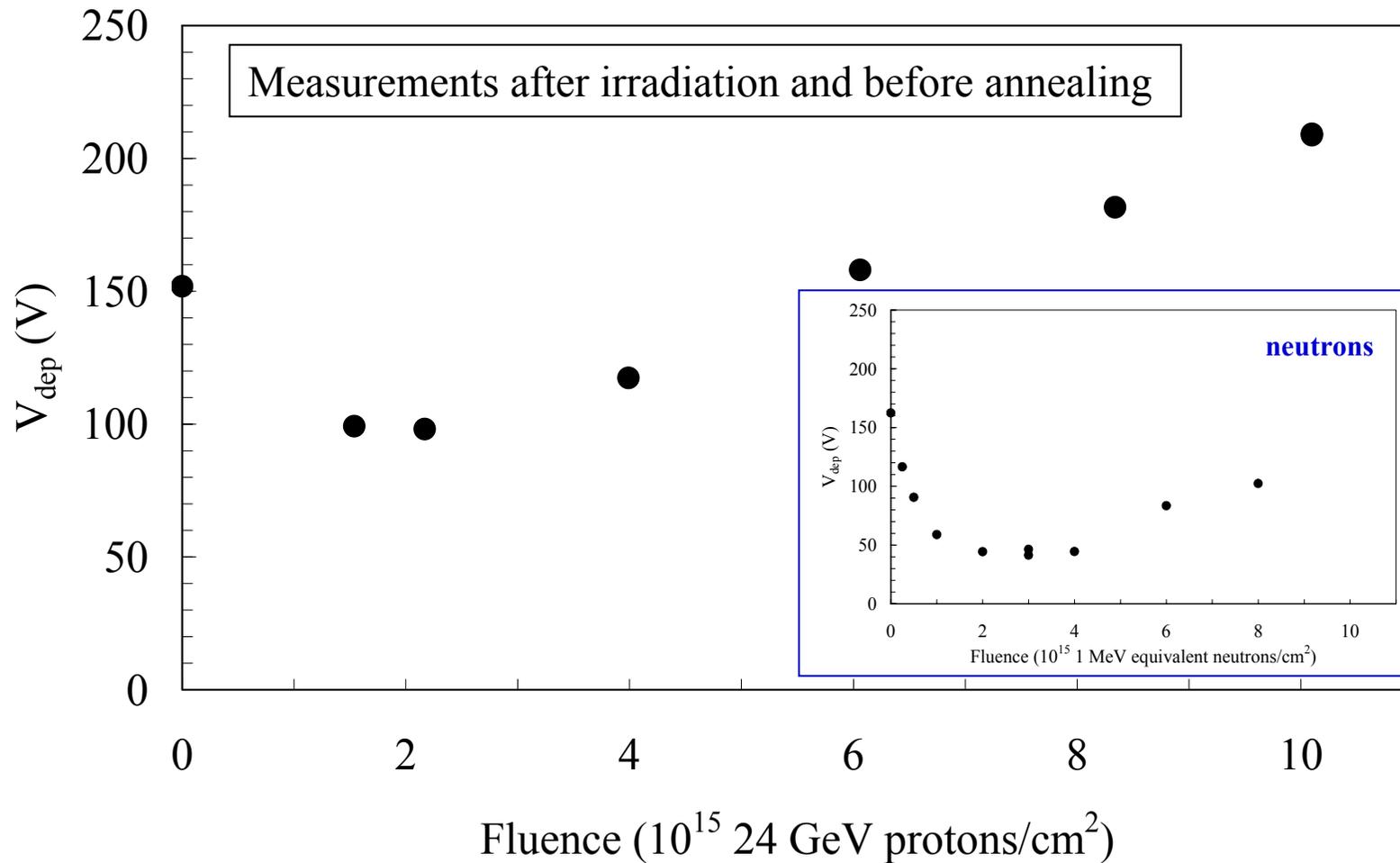
- $J_D$  increases linearly with fluence.
- $J_D$  decreases with annealing time at 80 °C.

# Epitaxial n-type 50 $\mu\text{m}$ thick detectors: neutron irradiation



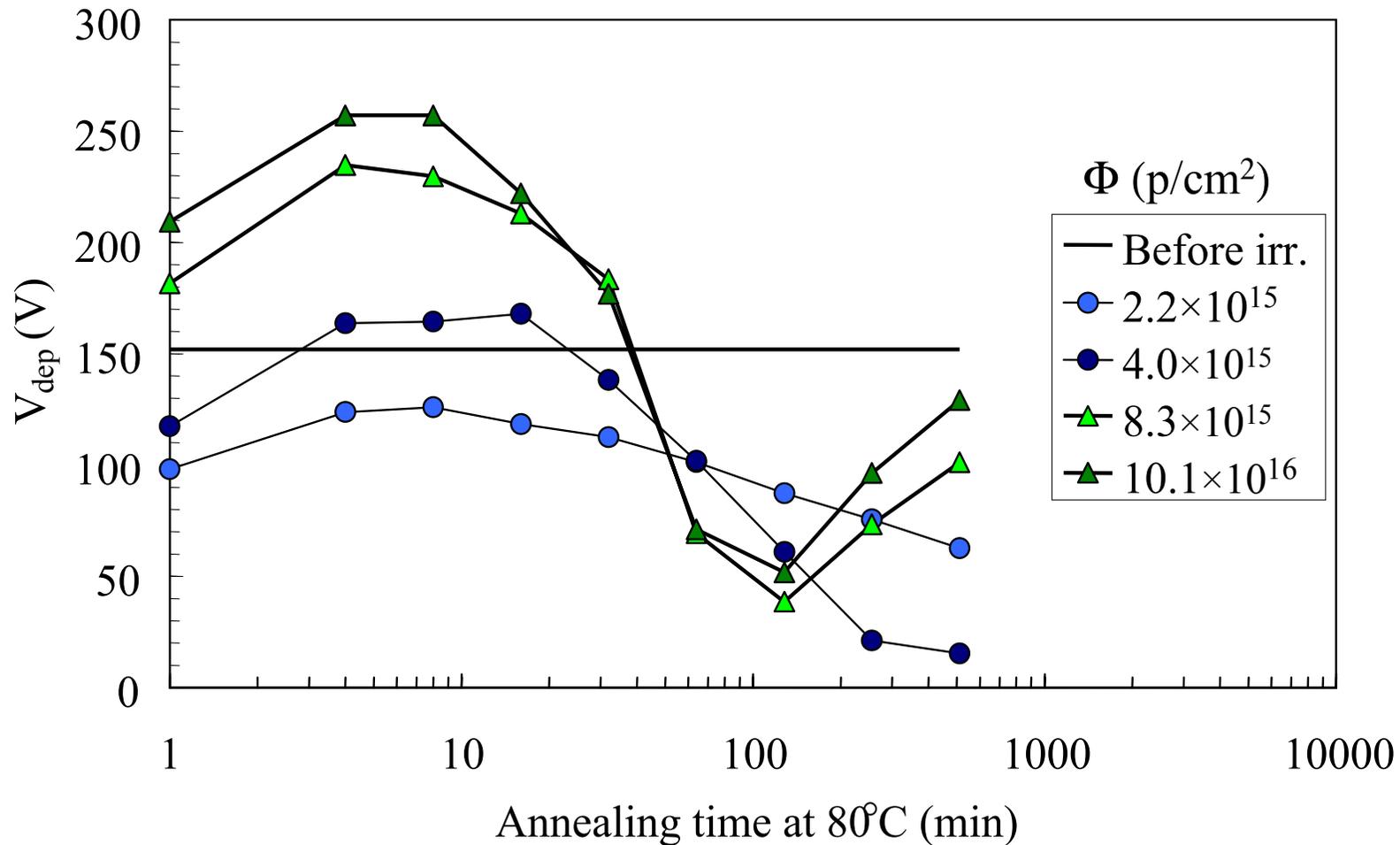
•  $J_D$  decreases with annealing time at 80 °C.

# Epitaxial n-type 50 $\mu\text{m}$ thick detectors: proton irradiation



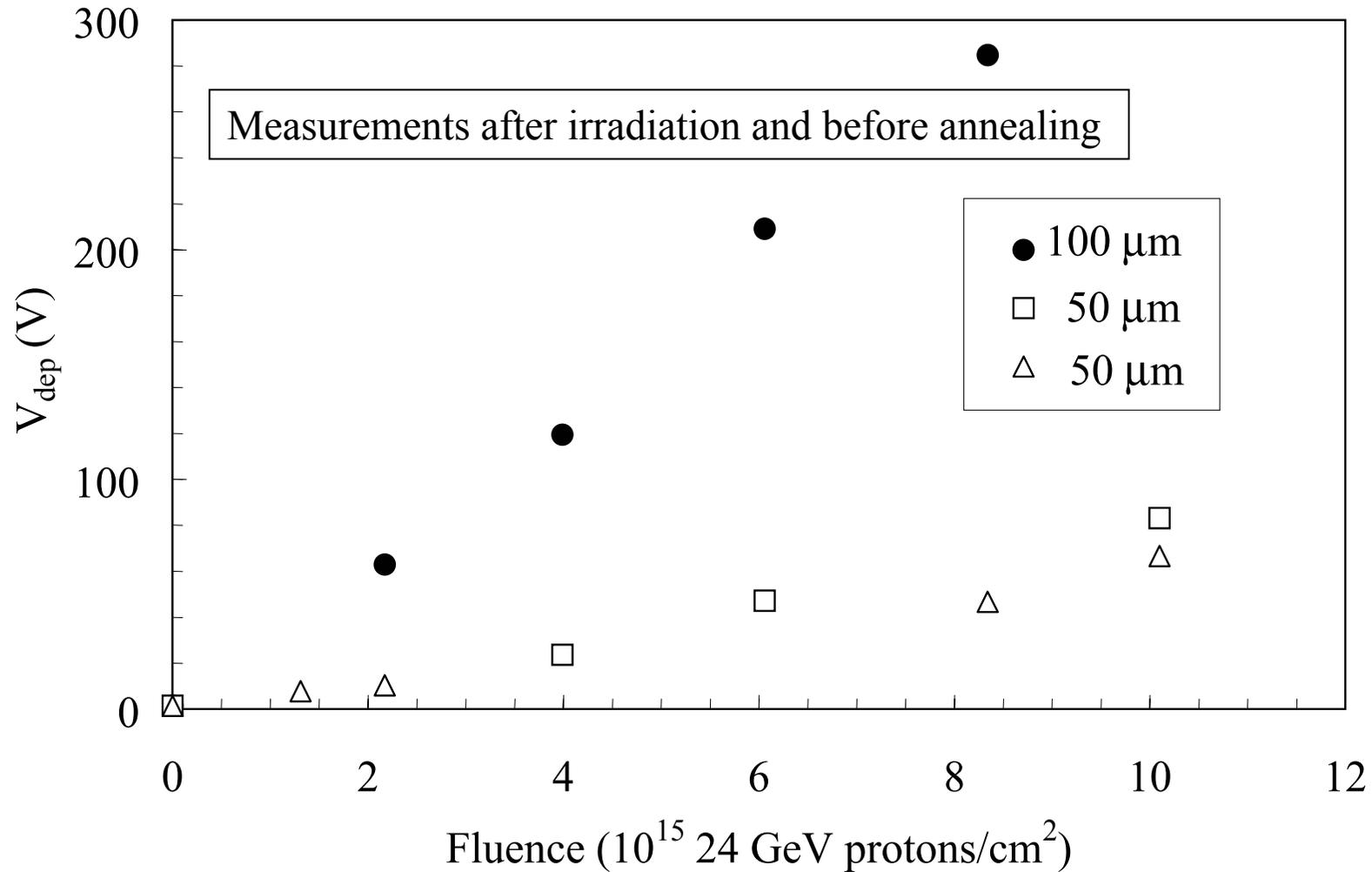
- The minimum of  $V_{\text{dep}}$  for protons (90-100 V) is higher than for neutrons (40-50 V).
- $V_{\text{dep}} > V_{\text{dep},0}$  at  $10^{16}$  p/cm<sup>2</sup>.
- The radiation effects induced by neutrons and protons are different.

## Epitaxial n-type 50 $\mu\text{m}$ thick detectors: proton irradiation



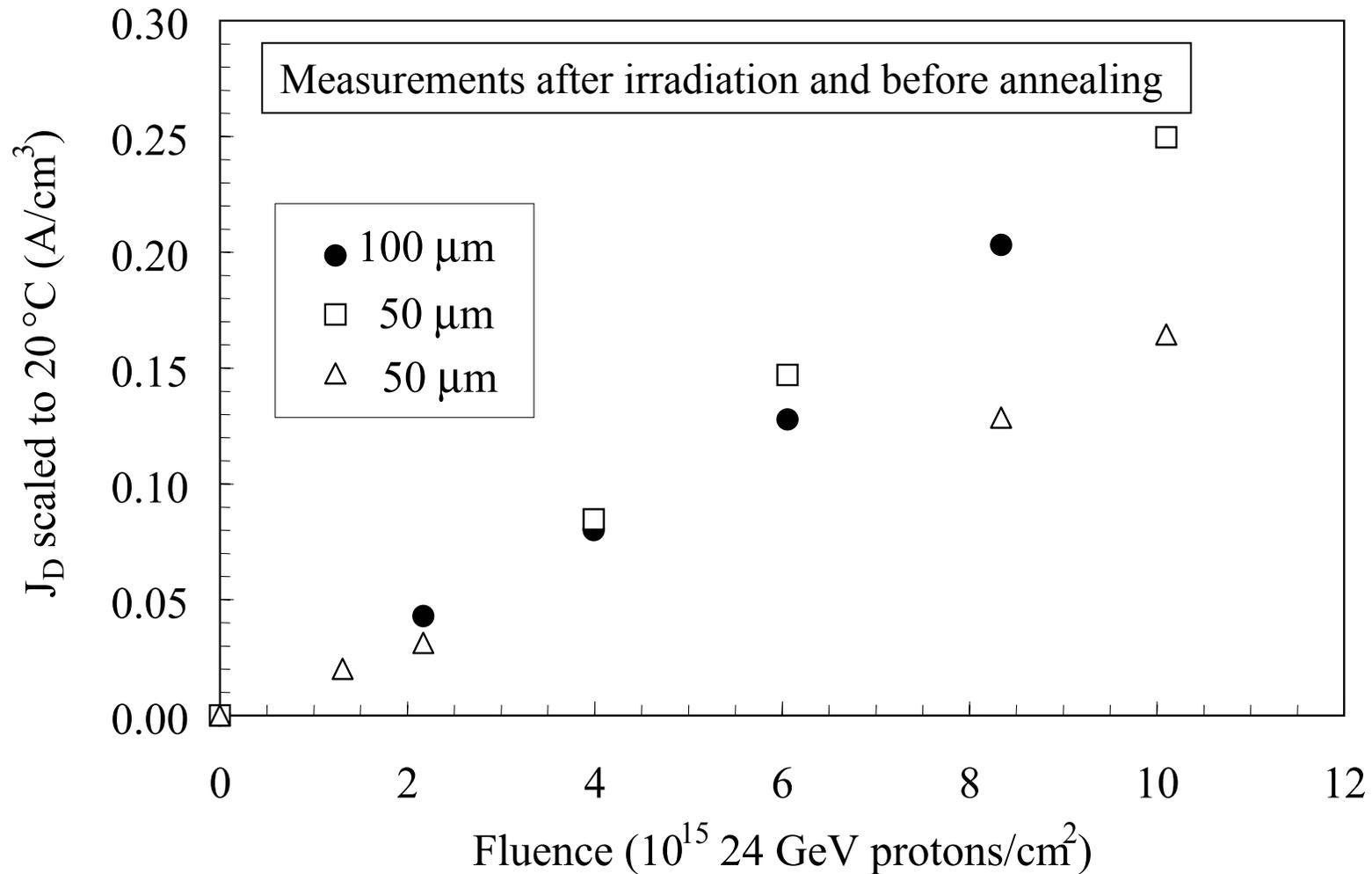
- $V_{\text{dep}}$  decreases for annealing times at  $80^\circ\text{C}$  higher than 8 minutes.
- If this effect is due to deep acceptor generation, devices are before SCSI after irradiation:
  - for devices irradiated at low fluences  $V_{\text{dep}} \uparrow, \downarrow$ : no SCSI during long-term annealing;
  - for devices irradiated at high fluences  $V_{\text{dep}} \uparrow, \downarrow, \uparrow$ : SCSI during long-term annealing.

# FZ thinned n-type detectors: proton irradiation



Device thinning limits the  $V_{\text{dep}}$  increase after irradiation:  
-  $V_{\text{dep}} \approx 290$  V for 100  $\mu\text{m}$  thick detectors at  $8 \times 10^{15}$  p/cm<sup>2</sup>  
-  $V_{\text{dep}} \approx 70$  V for 50  $\mu\text{m}$  thick detectors at  $10^{16}$  p/cm<sup>2</sup>

## FZ thinned n-type detectors: proton irradiation



- $J_D$  linearly increases with fluence, independently on the thickness. (some data dispersion is present)

# DETECTORS: future activity

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## CERN, 24 GeV protons, May 2004 (n-type materials)

Fluence (24 GeV protons/cm <sup>2</sup> )	CZ n-type	MCZ n-type	Epitaxial n-type	Thinned FZ n-type	
	300 um	300 um	50 um	50 um	100 um
5.10E+14	1	1	---	---	---
1.31E+15	---	---	---	1	---
1.54E+15	1	1	1	---	---
2.17E+15	1	1	1	1	1
3.99E+15	1	1	1	1	1
6.06E+15	1	1	1	1	1
8.34E+15	---	---	1	1	1
1.01E+16	---	---	1	2	---
<b>Total</b>	<b>5</b>	<b>5</b>	<b>7</b>	<b>7</b>	<b>4</b>

## Lubiana, nuclear reactor neutrons, December 2004 (n- and p-type materials)

Fluence (1 MeV equivalent neutrons/cm <sup>2</sup> )	CZ n-type	MCZ n-type	Epitaxial n-type	FZ p-type	MCZ p-type
	300 um	300 um	50 um	200 um	300 um
1.00E+14	2	1	---	0	0
1.50E+14	2	1	---	1	1
2.00E+14	2	2	---	2	2
2.50E+14	2	1	1	1	1
5.00E+14	2	1	1	2	2
1.00E+15	2	1	2	1	1
2.00E+15	---	---	2	2	2
3.00E+15	---	---	3	0	0
4.00E+15	---	---	2	---	---
6.00E+15	---	---	2	---	---
8.00E+15	---	---	2	---	---
<b>Total</b>	<b>12</b>	<b>7</b>	<b>9</b>	<b>9</b>	<b>9</b>

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# ELECTRONICS

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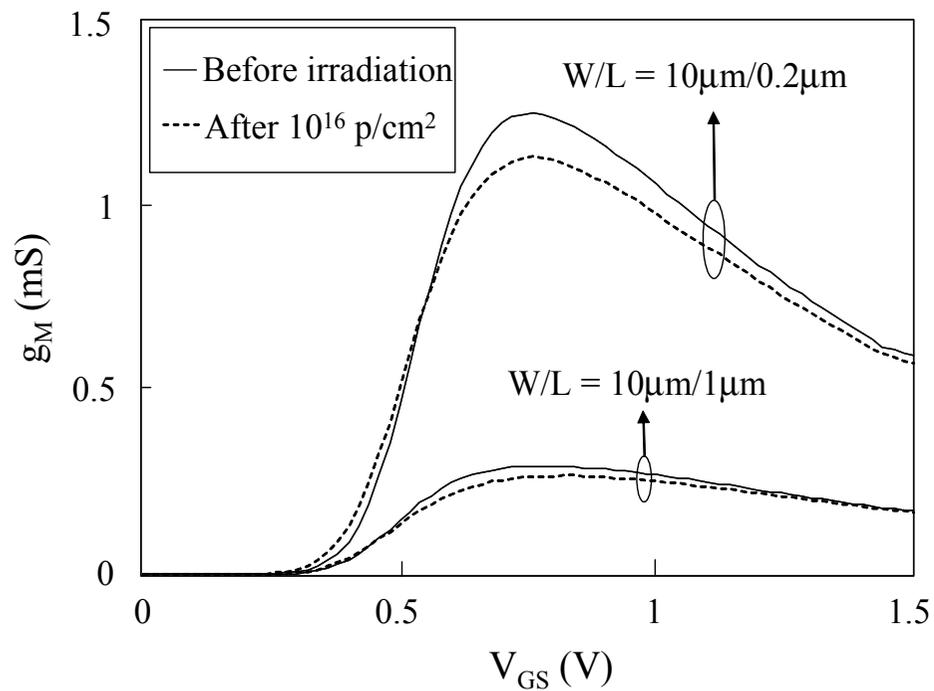
- Electronics for LHC** (10 Mrad(Si) and  $10^{15}$  fast hadrons/cm<sup>2</sup>):
- **0.25  $\mu\text{m}$  CMOS** technology from **IBM**;
  - **5.5 nm** oxide thickness;
  - radiation hardened by design: **enclosed geometry** transistors.

**The 0.25  $\mu\text{m}$  CMOS technology will not be any more available for Super-LHC**

- Electronics for Super-LHC** (100 Mrad(Si) and  $10^{16}$  fast hadrons/cm<sup>2</sup>):  
following the scaling down of the commercial CMOS technologies?
- **0.13  $\mu\text{m}$**  minimum channel length;
  - **2.5 nm** oxide thickness;
  - no radiation hardened by process;
  - no radiation hardened by design (i.e., no enclosed geometry).
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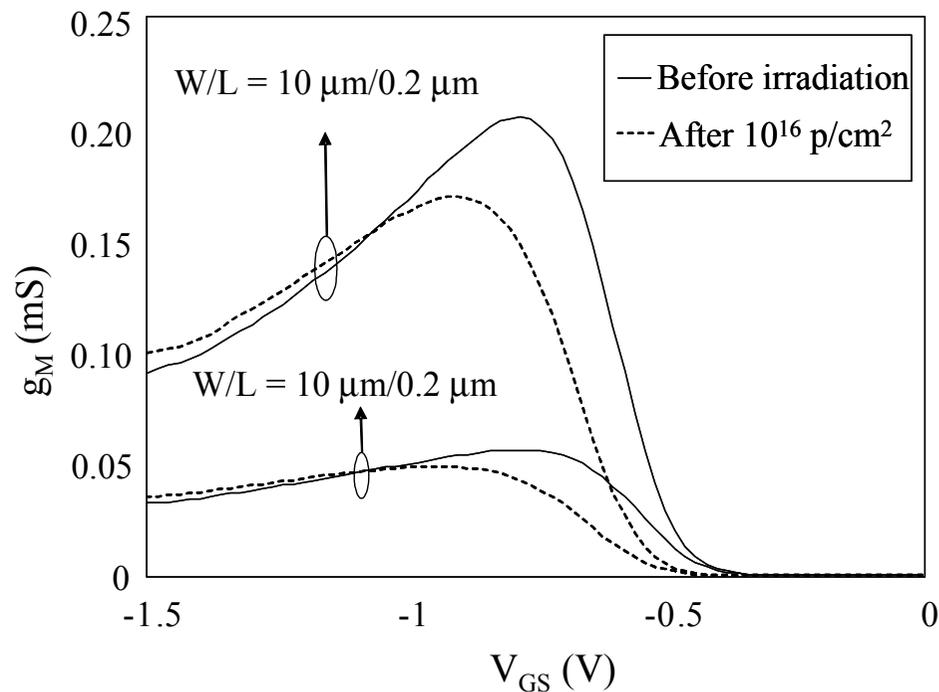
# 0.13 $\mu\text{m}$ CMOS technology: proton irradiation

n-channel MOSFET



- $g_M$  peak decrease 15%
- $g_M$  peak shift negligible

p-channel MOSFET



- $g_M$  peak decrease higher than in n-MOSFET
- $g_M$  peak shift of 200 mV